

Configuration 1

FIG. 1A

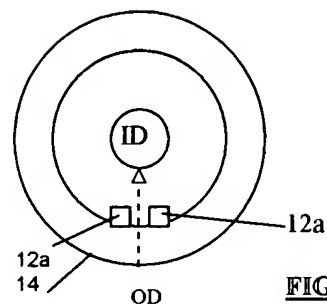


FIG. 1B

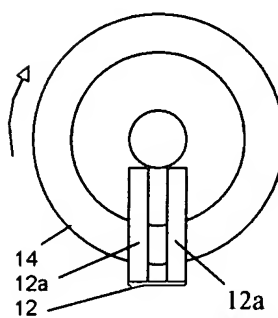
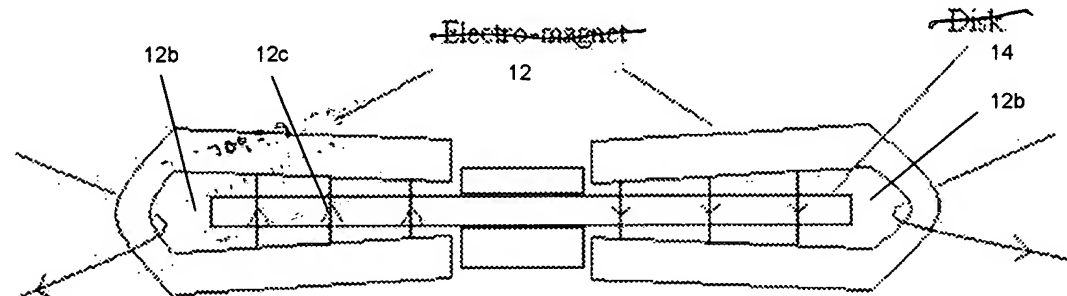


FIG. 1C



Configuration 2

FIG. 2A

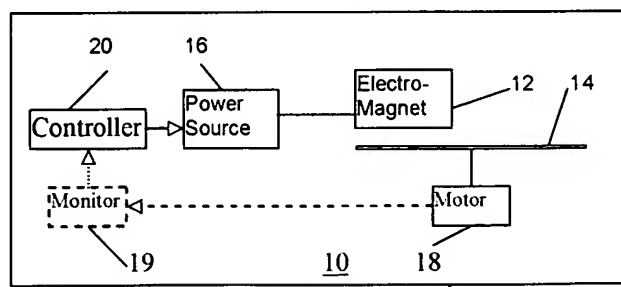


FIG. 2C

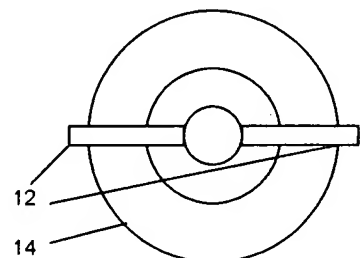


FIG. 2B

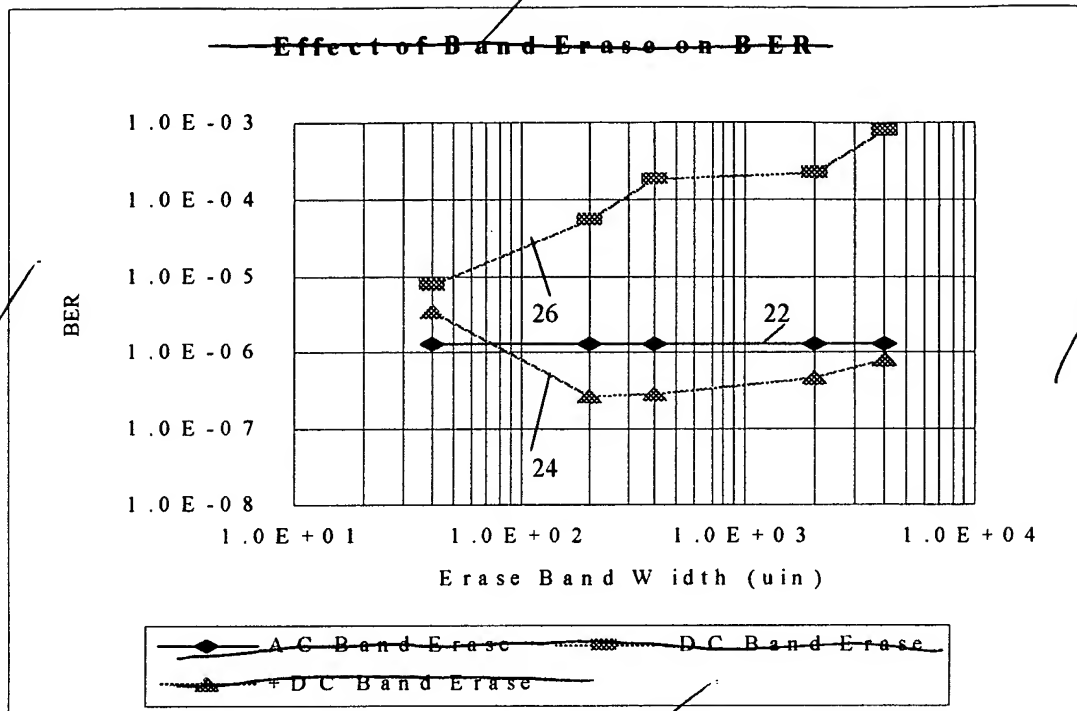


FIG. 3A - ~~Effect of Band Erase on BER~~

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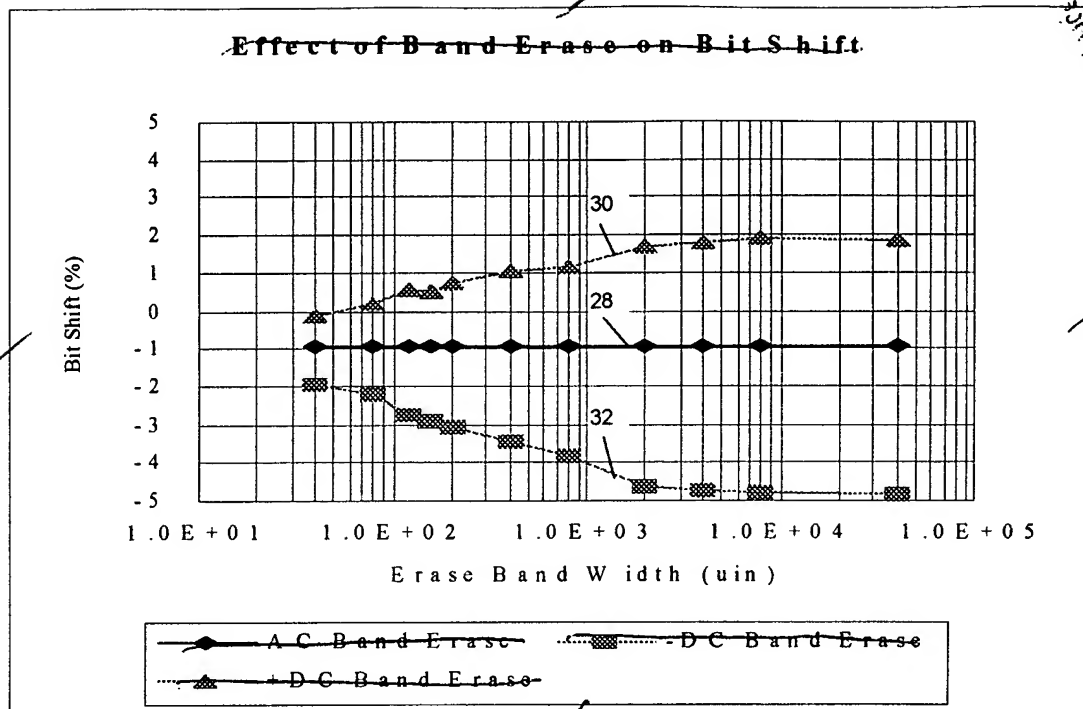


FIG. 3B - ~~Effect of Band Erase on Transition Shift~~

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Patented 1997
U.S. Pat. No. 5,540,000
Inventors: R. M. HARRIS, JR.
Attorney: R. M. HARRIS, JR.

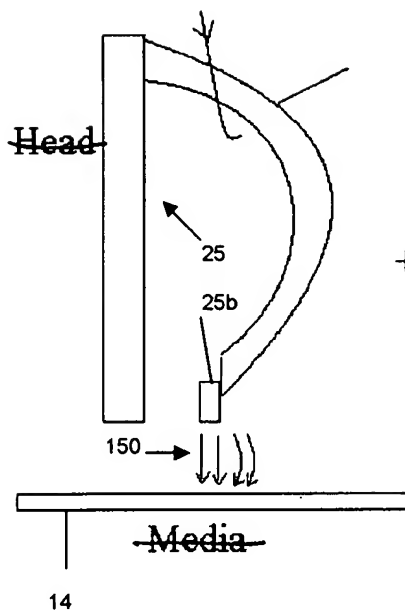


FIG. 4C (AC-Erase)

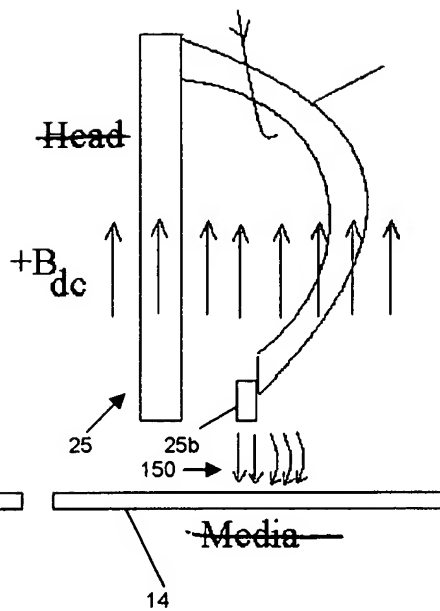


FIG. 7B (+DC-Erase)

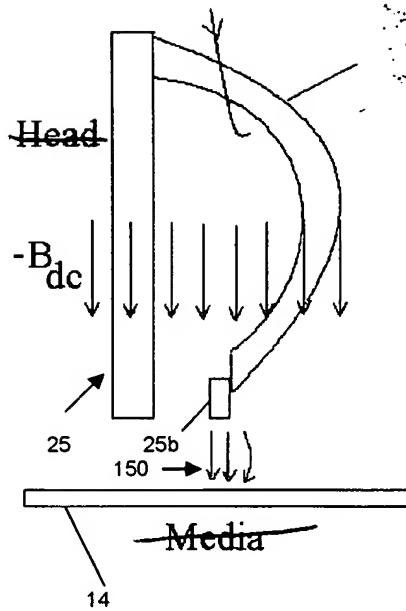


FIG. 7C (-DC-Erase)

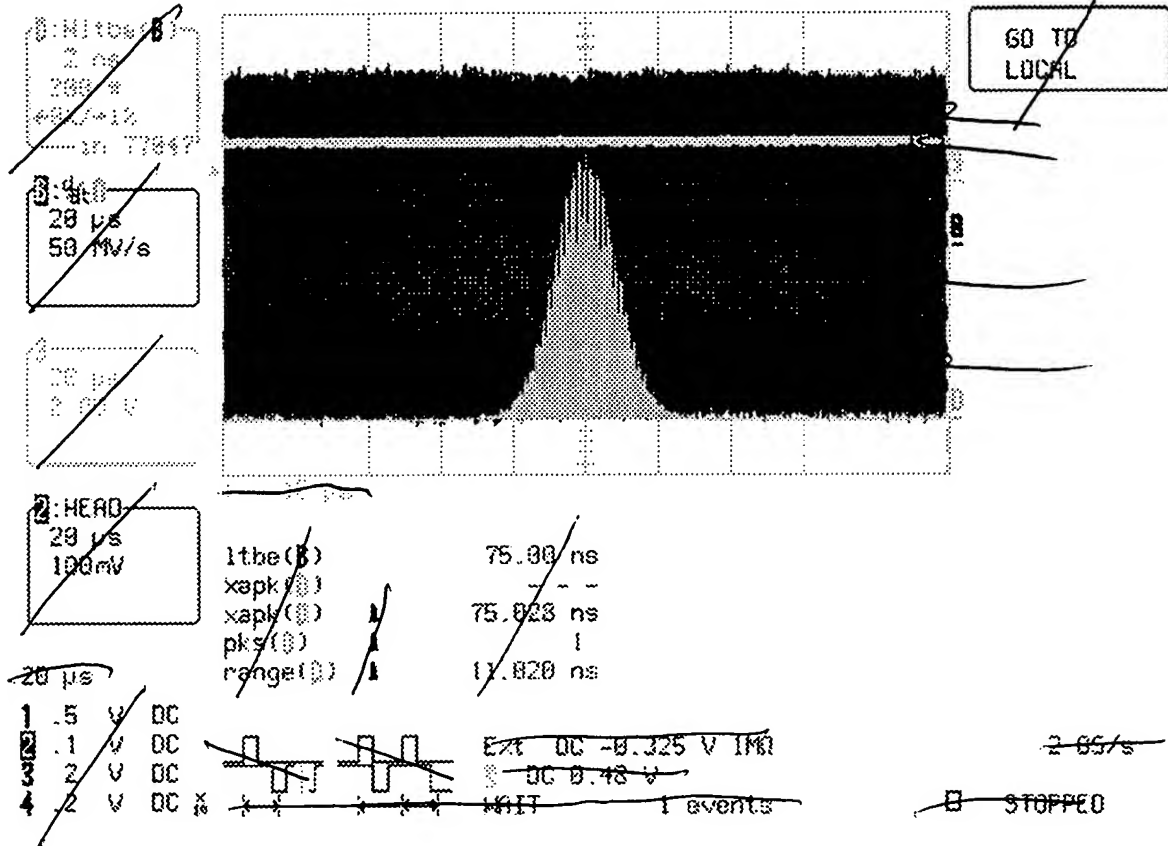


FIG. 6B Read back signal measurement for a track written on as-received media from disk sputtering process without any net magnetization, wherein timing asymmetry is eliminated.

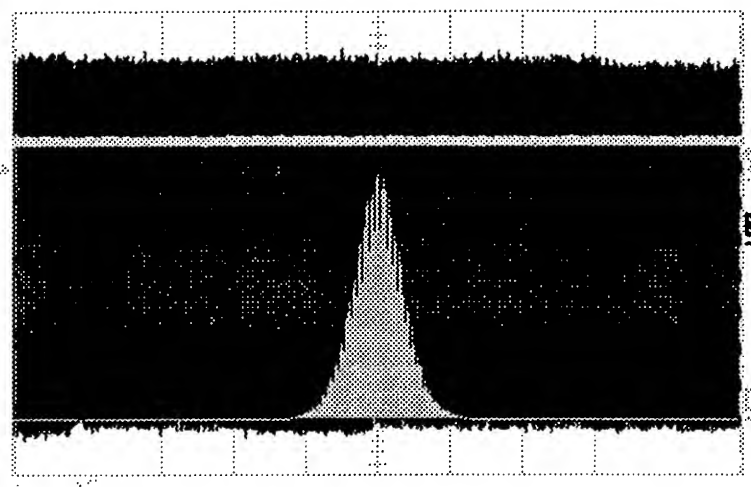
0: HITOS (B)
 2.00
 8.00 ka
 2.00
 10 100143

3.4 ns
 28 ps
 50 MV/s

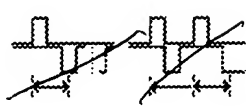
70 ps
 2.00 V

2: HEAD
 20 ps
 100 mV

20 ps
 1 5 V DC
 2 5 V DC
 3 2 V DC
 4 2 V DC



ltbe(B) 75.90 ns
 xepk(B) - - -
 xapk(B) 74.929 ns
 pkz(B) 1
 range(B) 9.250 ns



EXT DC -0.325 V 1MBT
 S DC 0.48 V
 WAIT 1 events

REMOTE ENABLE

GO TO LOCAL



2 GS/s

STOPPED

FIG. 7D Measurement of data written on media preconditioned by DG erasing with alternate polarity on adjacent tracks, wherein timing asymmetry is eliminated.